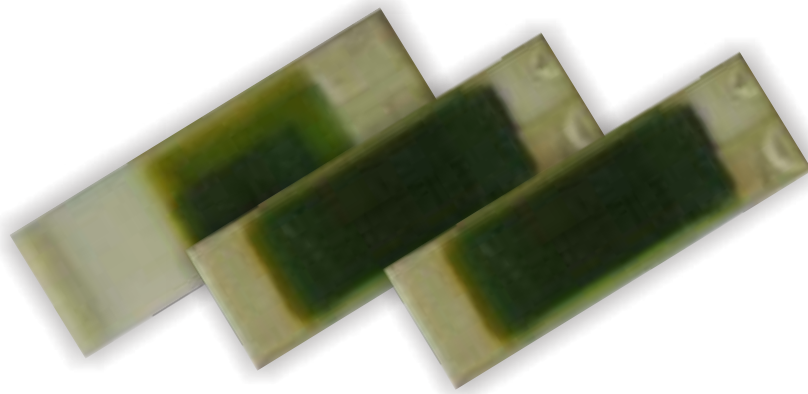
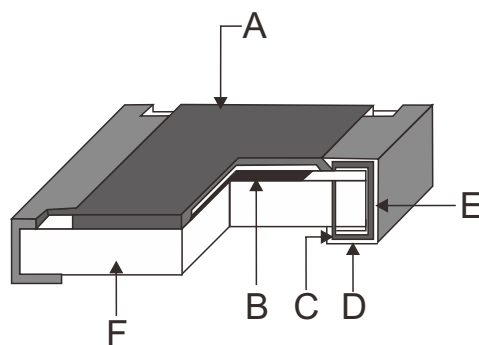


CST 双面贴片电阻 CHIP RESISTORS (CST)



● 产品结构图 Construction Drawing



A	B	C	D	E	F
保护膜 Protective coating	电阻体 Resistive film	内电极 Inner electrode	中间电极 (镀镍层) Middle electrode (Ni Plating)	外部电极 (底镍层) Outer electrode (Sn Plating)	陶瓷基体 Ceramic substrate

● 特性 Feature

(1)小型轻量、大幅缩小PCB板面积及重量。

It can reduce the area and weight of the PCB for its small size and light.

(2)使用环境温度-55℃~+155℃

Operating ambient temperature:-55℃~+155℃

(3)高性赖性：金属电阻体加玻璃保护层及三层电极构造，可靠度高。

High credibility: the thick metal film resistor body is added with glass protective layer and three layers of electrode composition.

(4)易装配性：外观尺寸均匀、精确、易于装配。

The appearance dimension is symmetrical and accurate for easy assembly.

(5)阻值误差

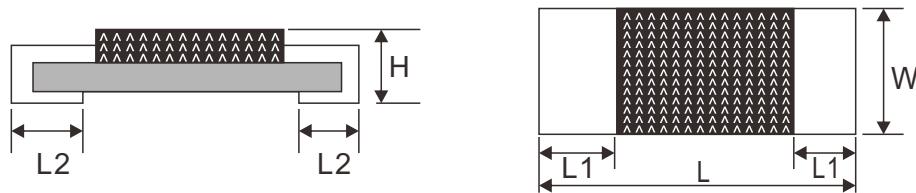
Resistance tolerance: $\pm 1\%$.

● 参考规格Reference Standards

JIS C 5201-1

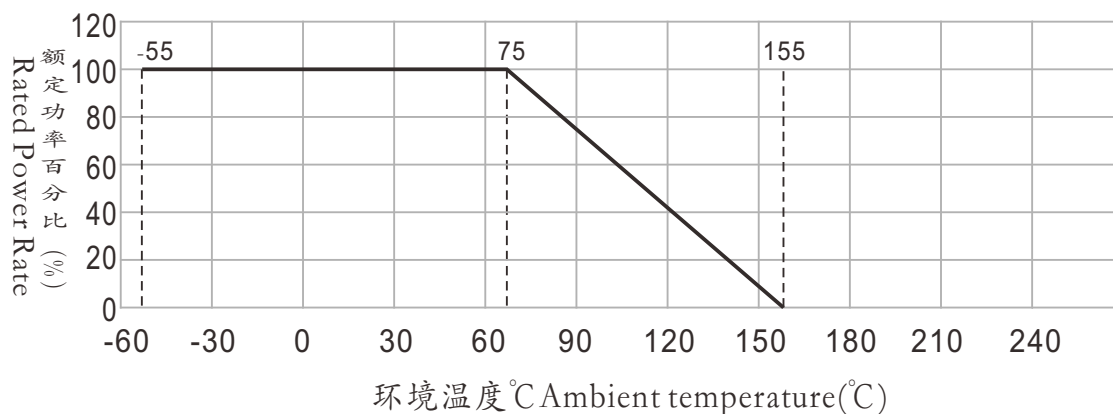
CST 双面贴片电阻 CHIP RESISTORS (CST)

外形尺寸 Dimensions



规格 Type	功率 Power	尺寸Dimensions(mm)					最高使用高压 Max. working voltage	最高负荷电压 Max. overload voltage	阻值范围 Resistance Range (Ω)
		L	W	H	L1	L2			
CST2510	0.5W	6.0±0.2	2±0.2	0.6±1.0	1.00±0.2	1.00±0.2	200V	400V	24.8M+124K

降功耗曲线 Derating Curve



性能 Performance

试验项目 Test Items	性能 Performance	试验方法 Test Methods(JIS C 5201-1)
温度系数 Temperature coefficient	±10ppm/°C	在常温及常温+100°C时分别测量电阻并计算每度的阻值变化率。 Test resistance value at normal temperature and normal temperature added 100°C, calculate °C resistance value change rate.
电压系数 Voltage Coefficient	±25ppm/V	按10V和100V电压, 分别施加在电阻上面, 并计算阻值变化率 According to 10 v and 100 v voltage, respectively on the resistance above, and calculating resistance rate
短时间过负荷 Short time overload	1%: ΔR ≤ ±(1%R ₀ +0.05Ω) 5%: ΔR ≤ ±(2%R ₀ +0.05Ω)	施加2.5倍额定功率或最高负荷电压(取较小者) 5秒 2.5X rated power or Max. overload voltage(get the lower) for 5seconds.
断续过负荷 Pulse overload	ΔR ≤ ±(1%R ₀ +0.05Ω)	4倍额定功率或最高断续负荷电压(取较小者) 测试1秒, 停止25秒, 循环10000 ±200次, At 4Xrated power or Max. pulse overload voltage(get the lower) cycle 10000 ±200 times(1second on 25seconds off)
电极强度(拉力) Terminal strength(pull)	ΔR ≤ ±(1%R ₀ +0.05Ω)	弯曲距离: 3mm(10秒) Bending distance:3mm(10 seconds)
耐焊接热 Resistance to soldering heat	ΔR ≤ ±(1%R ₀ +0.05Ω)	在350±10°C的锡炉中浸入2~3秒。 Immerge into the 350±10°C tin stove for 2~3 seconds
可焊性 Solderability	焊锡面积覆盖率95%以上 Tth soldering area is over 98%	在245±3°C的锡炉中浸入2~3秒。 Immerge into the 245±3°C tin stove for 2~3 seconds
温度循环 Temperature cycle	1%: ΔR ≤ ±(0.5%R ₀ +0.05Ω) 5%: ΔR ≤ ±(1%R ₀ +0.05Ω)	在-55°C时放置30分钟, 然后在+25°C时放置10~15分钟, 然后再在+125°C时放置30分钟, 然后在25°C时放置10~15分钟, 共循环5次。At -55°C for 30min, then at +25°C for 10~15min, then at +125°C for 30min, then at +25°C for 10~5, min, total 5cycles.
耐湿负荷寿命 Load life in humidity	1%: ΔR ≤ ±(1%R ₀ +0.1Ω) 5%: ΔR ≤ ±(3%R ₀ +0.1Ω)	在温度为40±2°C, 相对湿度为90~95%的恒温恒湿箱中, 施加额定电压或最大工作电压(取较小者)共1000小时(通1.5小时, 断0.5小时)。Overload rated voltage or Max.working voltage(get the lower) for 1000hours(1.5hours on and half-hour off) at the 40 ±2°C and 90~95% relative humidity.
耐温负荷寿命 Load life in heat	1%: ΔR ≤ ±(1%R ₀ +0.1Ω) 5%: ΔR ≤ ±(3%R ₀ +0.1Ω)	在70±2°C恒温恒湿箱中施加额定电压或最大工作电压(取较小者)共1000小时(通1.5小时, 断0.5小时)。Overload rated voltage or Max.working voltage(get the lower) for 1000hours(1.5hours on and half-hour off) at the 70±2°C.

● 料号编号 ordering Information

例 example

CST	2510	05	F	I	100K0
产品名称 Product Name	尺寸 Size	功率 Power	精度 Tol	包装 Packing	阻值 Ohm
贴片电阻器 Chip Resistors		05=0.5W	F=±1%	1=10KPCS 4=4KPCS 5=5KPCS	0R100=0.1Ω 0R220=0.22Ω 10R00=10KΩ 1M000=1MΩ